

AMENDMENTS TO THE SPECIFICATION:

Please replace the paragraph beginning at page 6, line 24, with the following rewritten paragraph:

- 1 In a preferred embodiment, the bus segments A and B are segments of a
- 2 single non-hierarchical PCI bus. Other bus configurations are possible, for
- 3 example an LPC (Low Pin Count) bus as defined by Intel 1997. The repeater
- 4 200 may use transaction decode as described above, or in another embodiment,
- 5 the repeater simply repeats all transactions over the bus that are not claimed by
- 6 any other device.

Please replace the paragraph beginning at page 7, line 4, with the following rewritten paragraph:

- 1 In other embodiments, the physical link can be another bus, such as the
- 2 LVDS, Gigabit Ethernet, InfiniBand, IEEE1394 serial bus, AC Link, LPC Link, or
- 3 a wireless link such as infrared or RF (radio frequency), or a combination of
- 4 these.